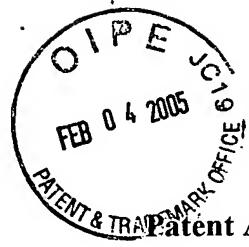


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Patent Application

Applicant(s): T.J. Gabara et al.

Case: 80-7

Serial No.: 10/079,447

Filing Date: February 20, 2002

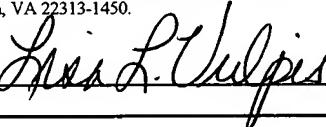
Group: 2825

Examiner: Granvill D. Lee

Title: Method for Modeling Noise Emitted by Digital Circuits

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I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature:  Date: February 2, 2005

RESPONSE TO FINAL OFFICE ACTION

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The remarks below are submitted in response to the third and final Office Action dated November 4, 2004 in the above-identified application. A Notice of Appeal is submitted concurrently herewith.